

PERSONAL INFORMATION

Luca Feltrin



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Website: <http://lucafeltrin.it>

Gender m | Birthday 25/03/1989 | Nationality Italian

EDUCATION AND TRAINING

from november 2015 – present day

Ph.D in Electronics, Telecommunications, and Information Technologies Engineering

Level 8 - QEQ

University of Bologna – DEI

Project Title: Long Range Low Power Wireless Communications Technologies for the IoT

Publications:

- L. Feltrin, R. De Bonis, S. Mijovic, C. Buratti, A. Stajkic, R. Verdone, “**Performance Evaluation of LORA Technology: Experimentation and Simulation**”, Proc. EuCNC 2016, June 27-30, Athens, Greece
- L. Feltrin, C. Buratti, E. Vinciarelli, R. De Bonis, R. Verdone, “**LoRaWAN: Evaluation of Link- and System-Level Performance**”, Submitted to IEEE Internet of Things Journal, April 2017

december 2014

Examination for the qualification to practice the profession of Engineer

University of Bologna

Final Mark: 207/240

from october 2011 – to february 2014

Master Degree in Electronics and Telecommunications Engineering for Sustainable Development

Level 7 - QEQ

University of Bologna – II faculty of Engineering in Cesena

Thesis Title: Design and implementation of a LDPC decoder on a GPU architecture

Final Mark: 110/110 with honors.

- Advanced elements of Calculus, Algebra and Physics
- Microelectronics, Semiconductors Physics, Photovoltaic cells
- Wireless and cable telecommunication network, cellular system planning
- Informative systems architecture, electronic calculators, programming of Chipcon microcontrollers
- Multimedia signal processing
- Elements of industrial automation and real time systems
- Parallel computation on GPU (CUDA)

from february 2013 – to july 2013

Erasmus

Delft University of Technology (Netherlands)

Courses taken:

- Digital Audio and Speech Processing
- Computer Arithmetic
- Vehicle Mechatronic
- VLSI system on chip
- Processor Design Project

from october 2008 – to october 2011

Bachelor Degree in Electronics and Telecommunications Engineering

Level 6 - QEQ

University of Bologna – II faculty of Engineering in Cesena

Thesis Title: Identification of Users and Objects in Smart Environment with Computer Vision Techniques

Final Mark: 110/110 with honors

- Calculus, Algebra, Physics, Chemistry, Signal Processing, Automatic Controls
- Analog and Digital Electronics
- Telecommunication and networks
- Electronic Calculators, Digital Systems and FPGA programming (Altera Cyclone II)
- Propagation and Electromagnetic Fields
- Informatics, Operative Systems, Distributed Systems
- Elements of Economy and Company Structure

from september 2002 – to july 2008

Diploma of industrial expert Chief in electronics and telecommunications

Level 5 - QEQ

Technical Industrial Institute G. Marconi

Final Mark: 100/100

- Elements of Electronics, Telecommunications and PCB design

WORK EXPERIENCE

from january 2015 – october 2015

Component Specialist Driver Station

Maserati S.p.a. – Via Ciro Menotti 322, 41122 – Modena (Mo), Italy

- Involved in Alfa Romeo's Giorgio Project
- Interface between Alfa Romeo and Suppliers for Components Software Developing

Business or sector Automotive

from april 2014 – to december 2014

SCADA Technical consultant for Alstom

Alten Italia S.p.a. (Bologna site) – Via Guelfa 5, 40138 – Bologna (Bo), Italy

- Developer of HMI applications for SCADA systems in the railways field
- Software Validation

Business or sector Consulting/Transport

PERSONAL SKILLS

Mother tongue
Other Languages

Italian

	UNDERSTANDING		SPEAKING		WRITING
	Listening	Reading	Interaction	Production	
Inglese	C1	C2	C1	C1	B2
IELTS: Mark 7.5, 22 April 2017					

Levels: A1/2: Basic user - B1/2: Independent user - C1/2 Proficient user
Common European Framework of Reference for Languages

IT Skills

- Great competence of the Microsoft Office package and others (OpenOffice)
- Good knowledge of C++, CUDA, C#, Java, VHDL, Matlab, and basic knowledge of HTML/JavaScript and PHP.